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Re: U.S. Utility Patent No. 6,950,355 B2; Issue Date: September 27, 2005
(from Appl. No. 10/020,208; Filed: December 18, 2001)
For: **System and Method for Screen Defect Related Reliability Failures in CMOS SRAMS**
Inventors: BHATTACHARYA *et al.*
Our Ref: 1875.0330001

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Request for Certificate of Correction under 37 C.F.R. § 1.322;
2. One (1) page of Certificate of Correction Form (PTO/SB/44); and
3. One (1) return postcard.

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

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MDS:mlb
Enclosures
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